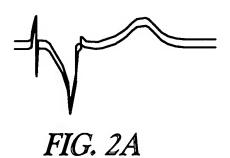
TITLE: METHOD AND APPARATUS FOR CAPTURE VERIFICATION AND THRESHOLD DETERMINATION INVENTOR'S NAME: DAVID TERNES, ET AL. SERIAL .NO.: 10/003,718 DOCKET NO.: 279.405US1 REPLACEMENT SHEET ~60 CAN 334 43A 534 MOS SWITCHES 25 PULSE GENREATOR PULSE GENREATOR PULSE GENREATOR 31/ 51. $\overline{\sim}$ EVOKED RESPONSE CHANNEL VENTRICULAR CHANNEL INTERFACE VENTRICULAR CHANNEL INTERFACE 23 81 7.50 S INTERFACE ATRIAL CHANNEL INTERFACE MICROPROCESSOR 10-5 INTERFACE CONTROLLER TELEMETRY MEMORY 330

INVENTOR'S NAME: DAVID TERNES, ET AL.
SERIAL.NO.: 10/003,718 DOCKET NO.: 279.405US1

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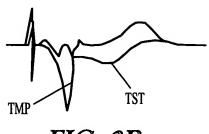


FIG. 2B

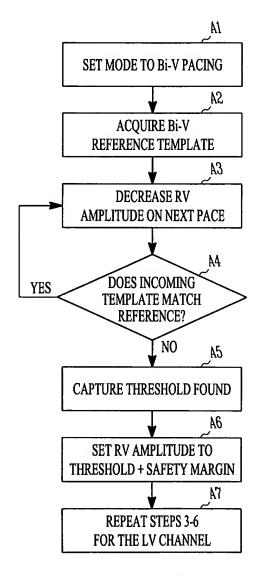


FIG. 3A

INVENTOR'S NAME: DAVID TERNES, ET AL. SERIAL. NO.: 10/003,718 DOCKET NO.: 279.405US1



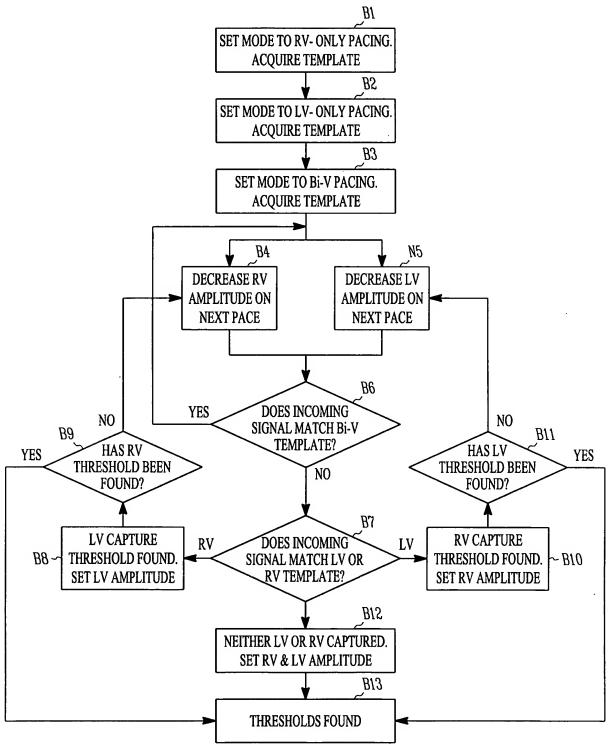


FIG. 3B

INVENTOR'S NAME: DAVID TERNES, ET AL.

SERIAL. NO.: 10/003,718 DOCKET NO.: 279.405US1

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REPLACEMENT SHEET

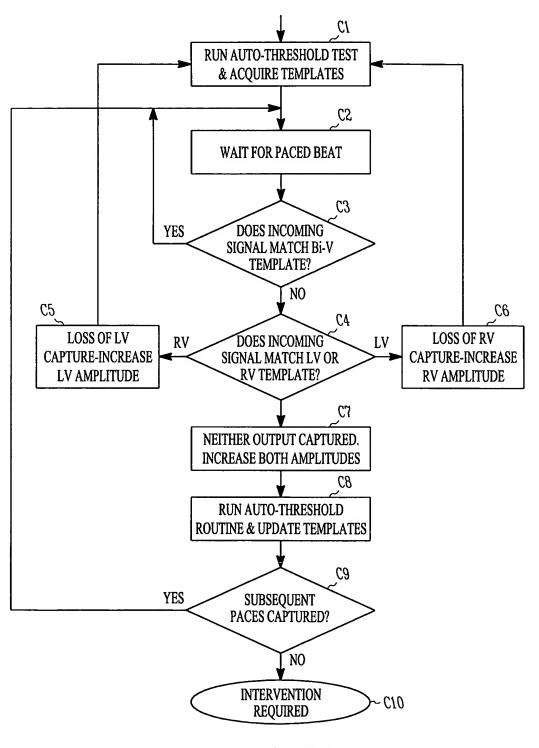


FIG. 3C

TITLE: METHOD AND APPARATUS FOR CAPTURE VERIFICATION AND THRESHOLD DETERMINATION INVENTOR'S NAME: DAVID TERNES, ET AL.

SERIAL NO.: 10/003,718 DOCKET NO.: 279.405US1

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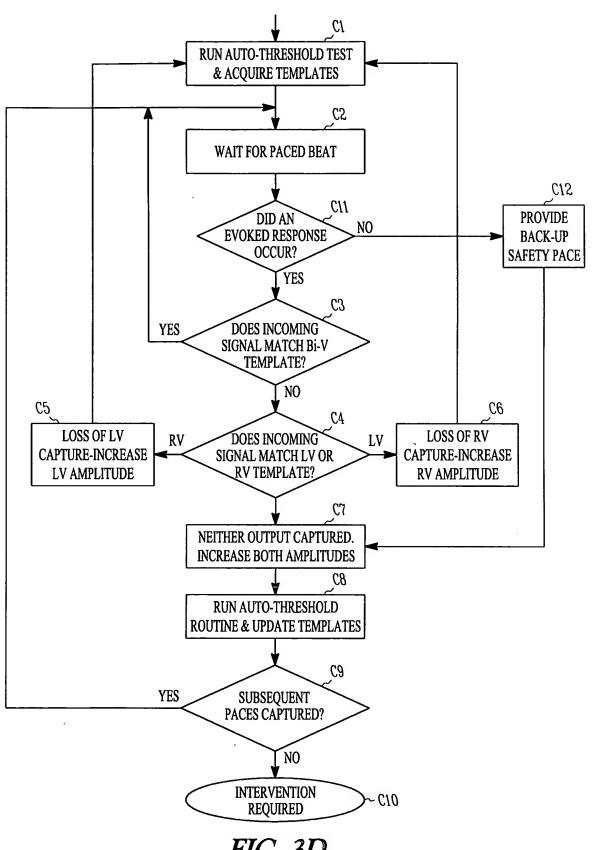


FIG. 3D